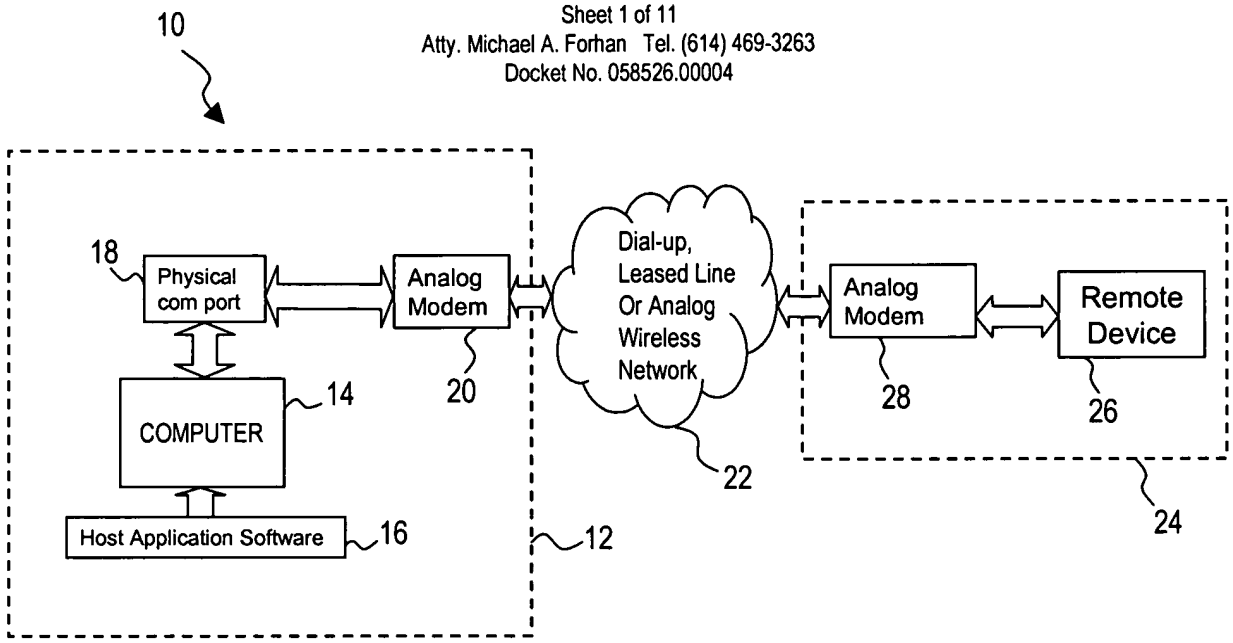


PRIOR ART

Fig. 1

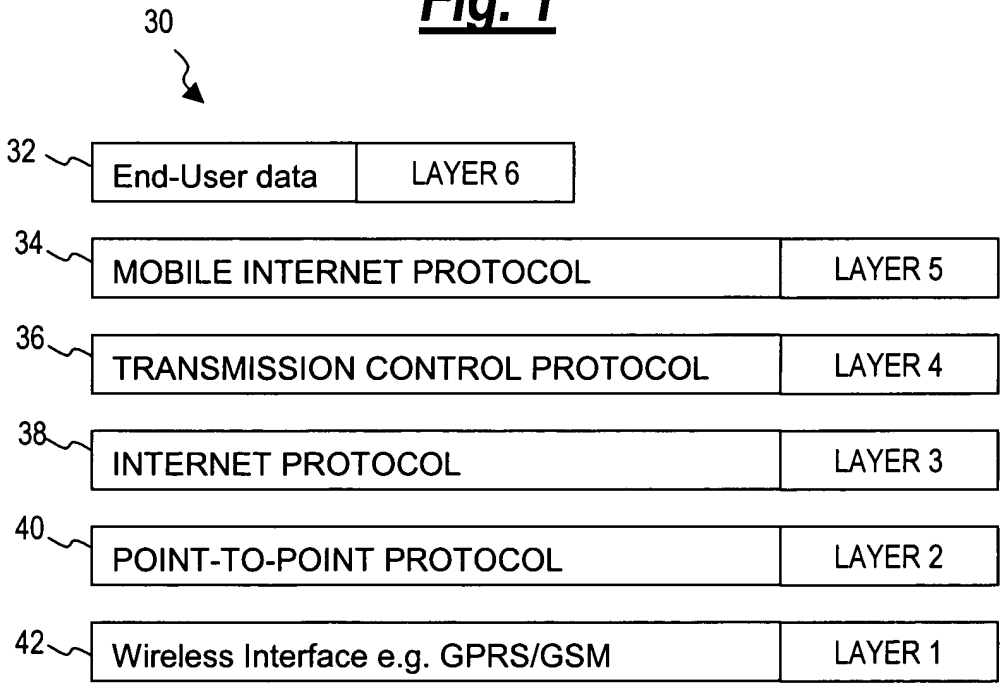
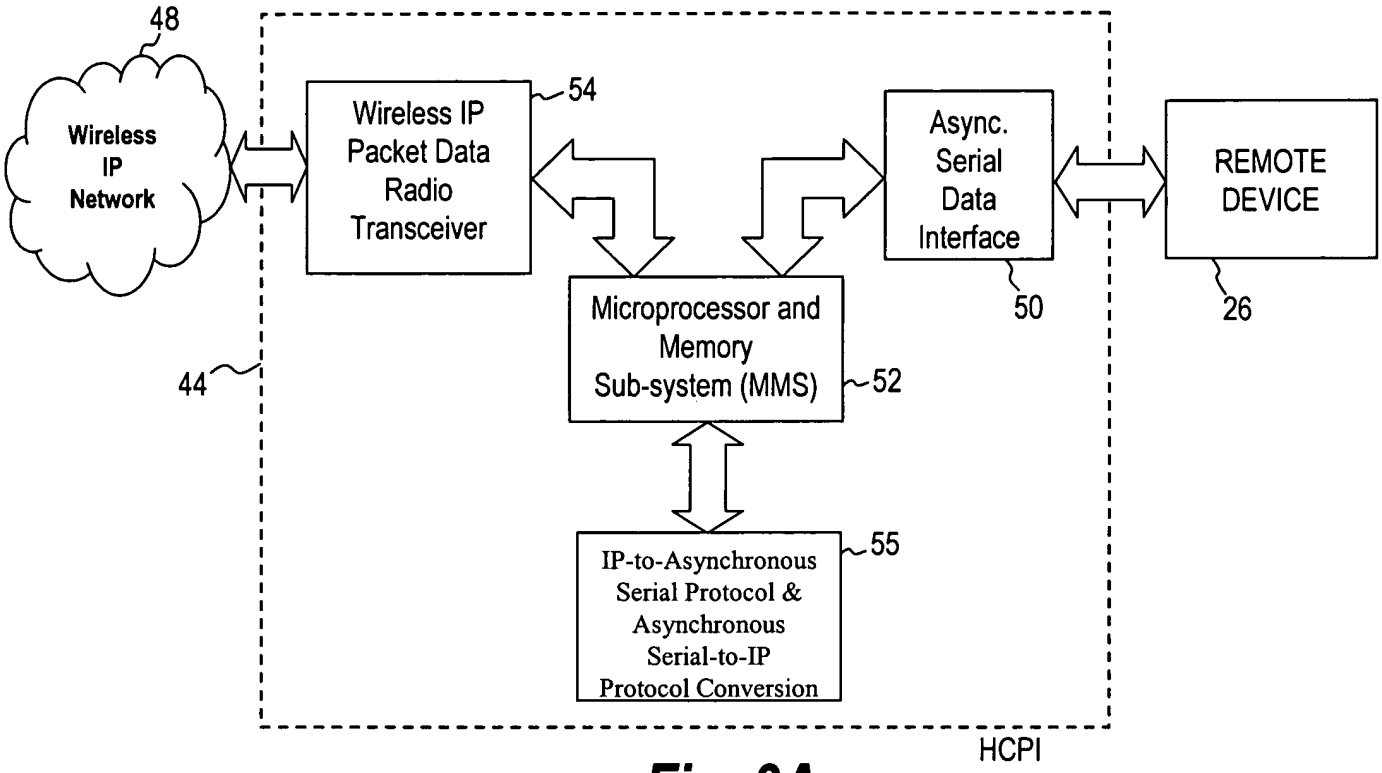


Fig. 2


48
Wireless IP Network
Wireless IP Packet Data Radio Transceiver
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Microprocessor and Memory Sub-system (MMS)
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IP-to-Asynchronous Serial Protocol & Asynchronous Serial-to-IP Protocol Conversion
55
HCPI

Fig. 3A

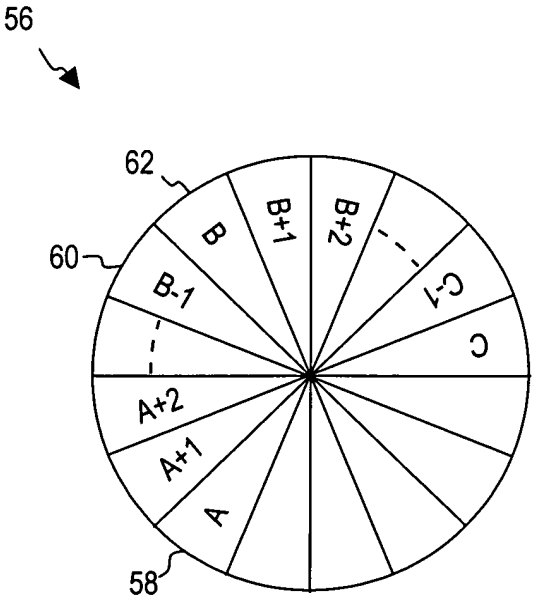
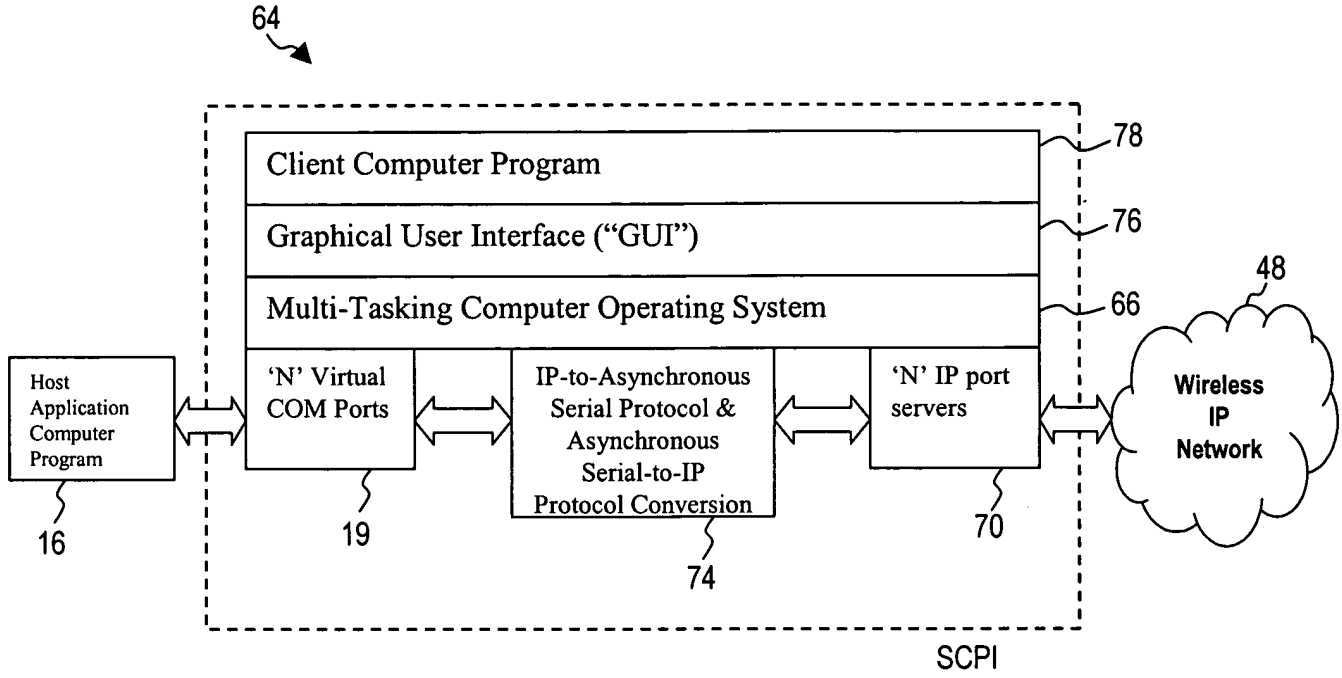

56
62
60
B
B+1
B+2
B-1
C-1
C
A+2
A+1
A
58

Fig. 3B


64
Client Computer Program
78
Graphical User Interface ("GUI")
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Multi-Tasking Computer Operating System
66
48
Host Application Computer Program
'N' Virtual COM Ports
IP-to-Asynchronous Serial Protocol & Asynchronous Serial-to-IP Protocol Conversion
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Wireless IP Network
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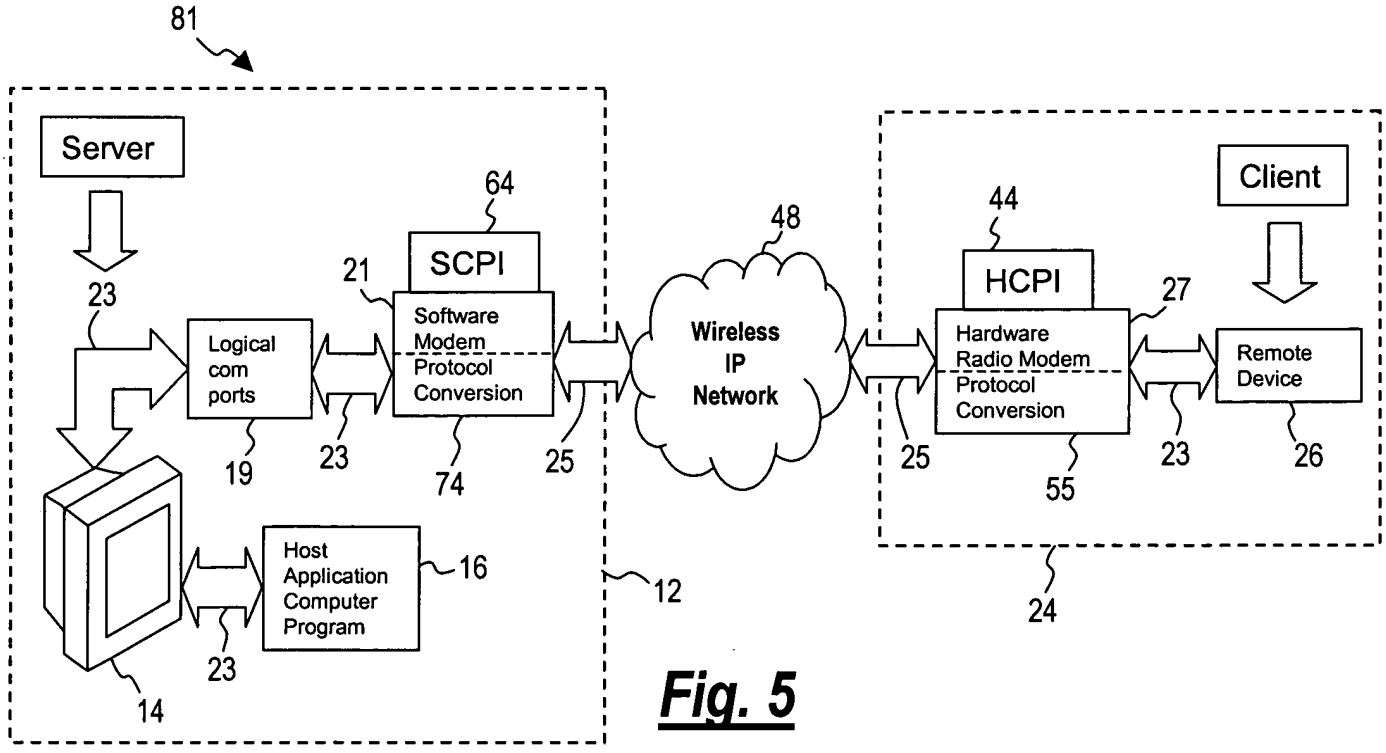
Fig. 4


81
Server
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Hardware Radio Modem Protocol Conversion
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Client
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Fig. 5

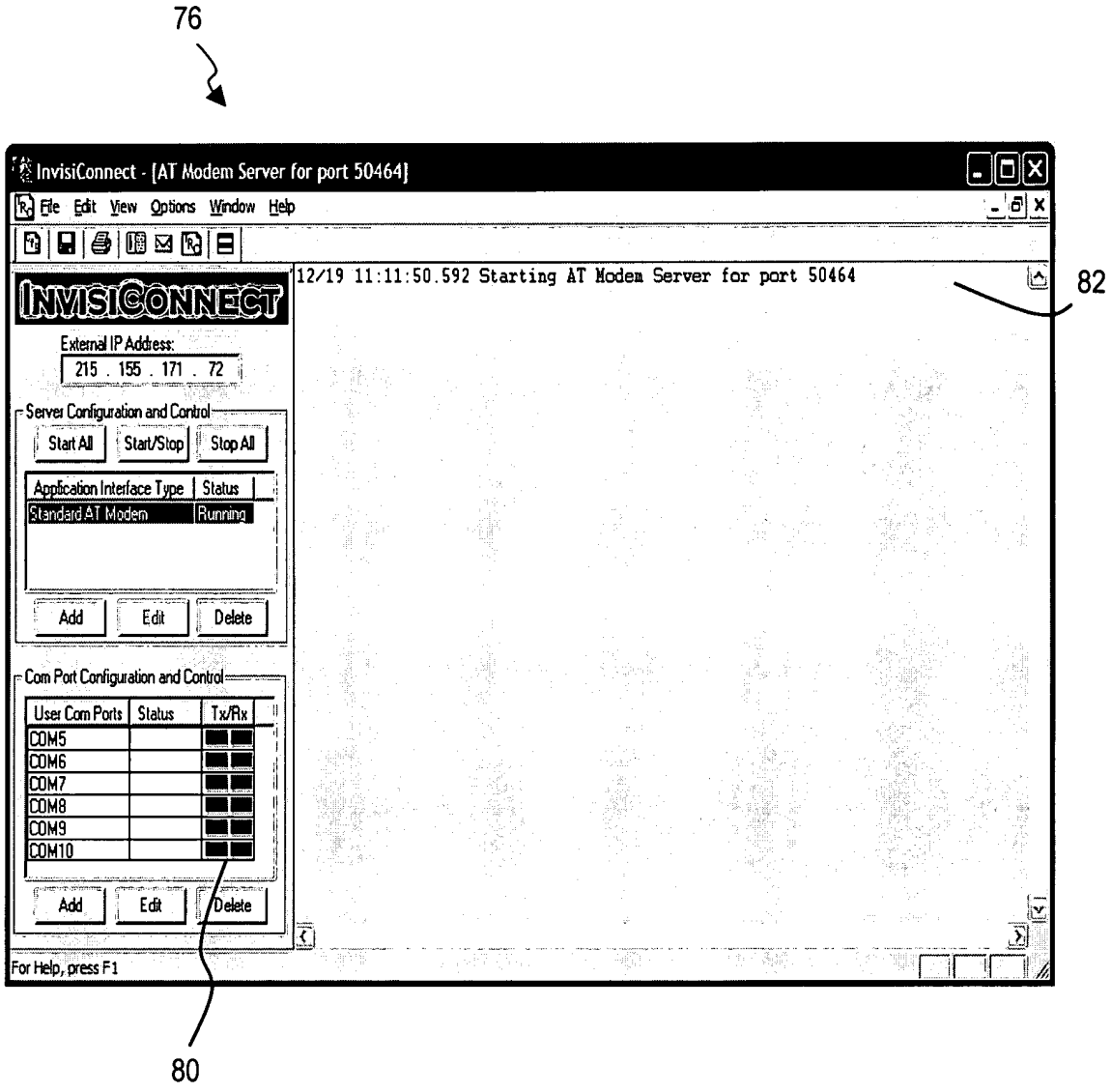

Fig. 6

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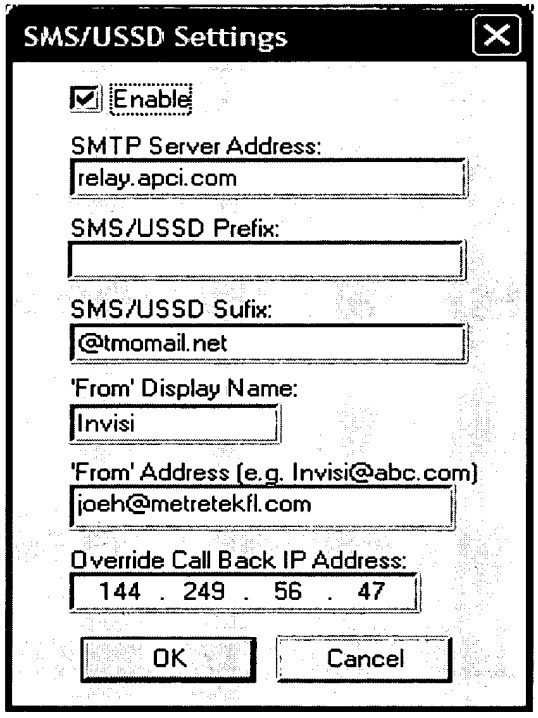

Fig. 7

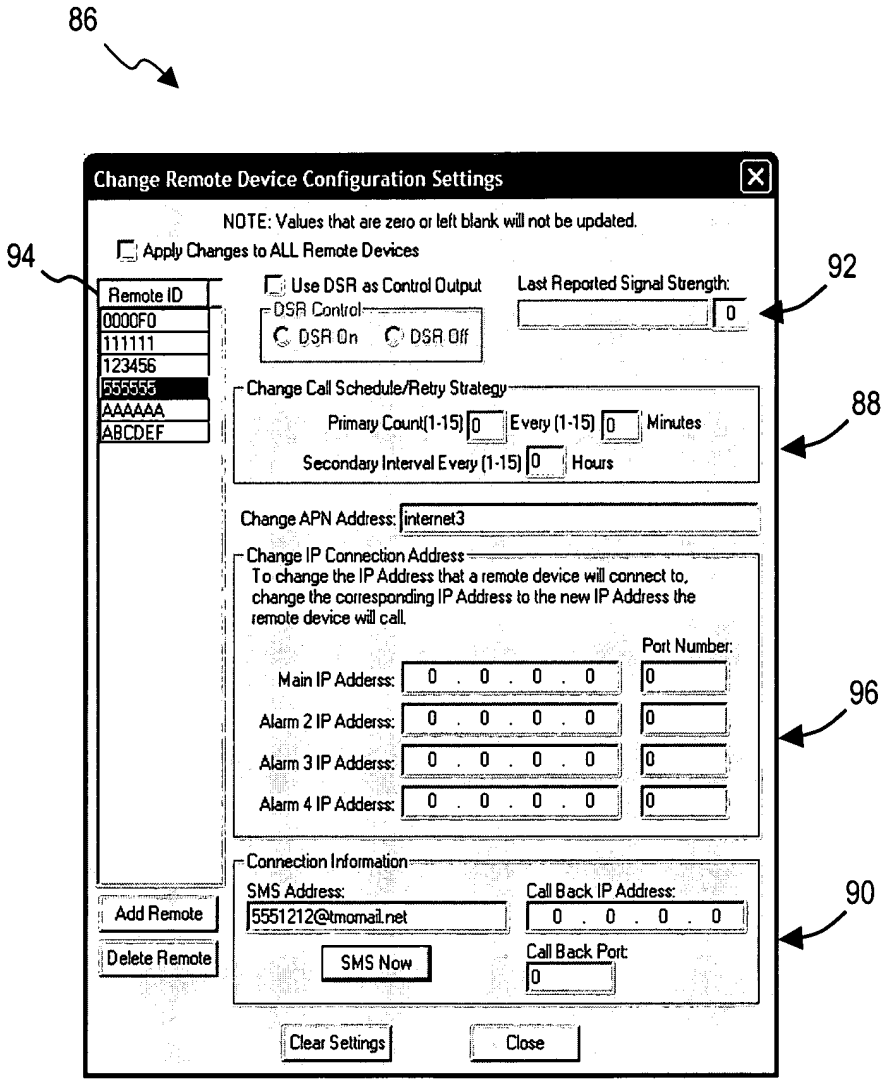

Fig. 8

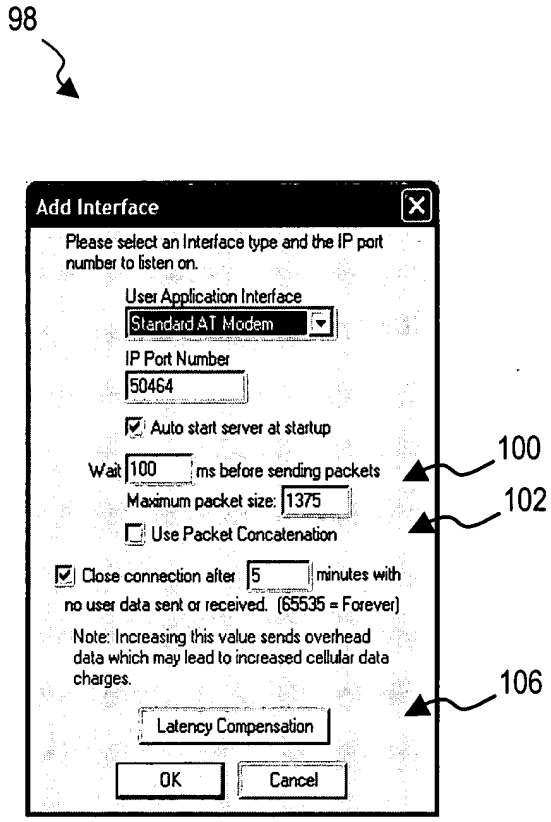

Fig. 9

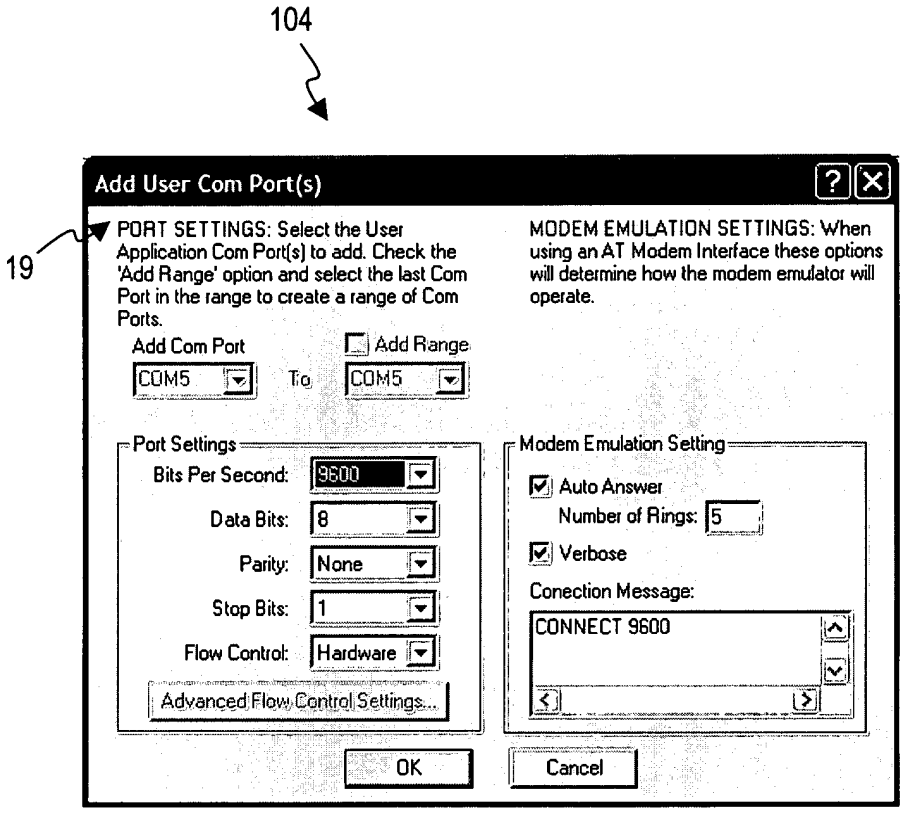

Fig. 10

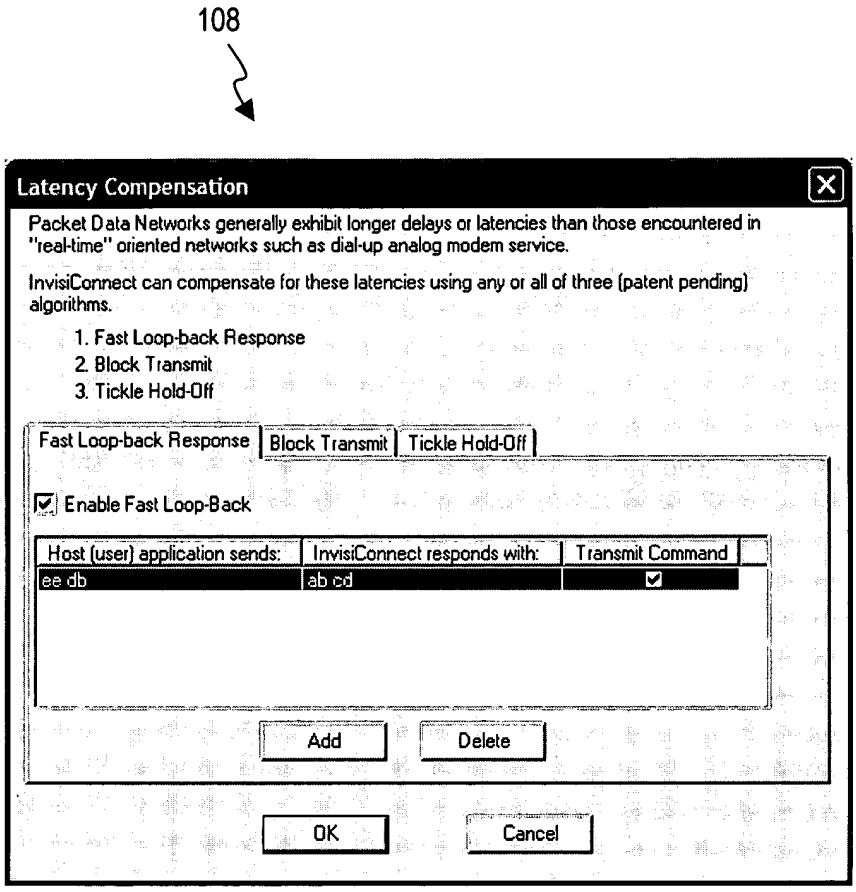

Fig. 11

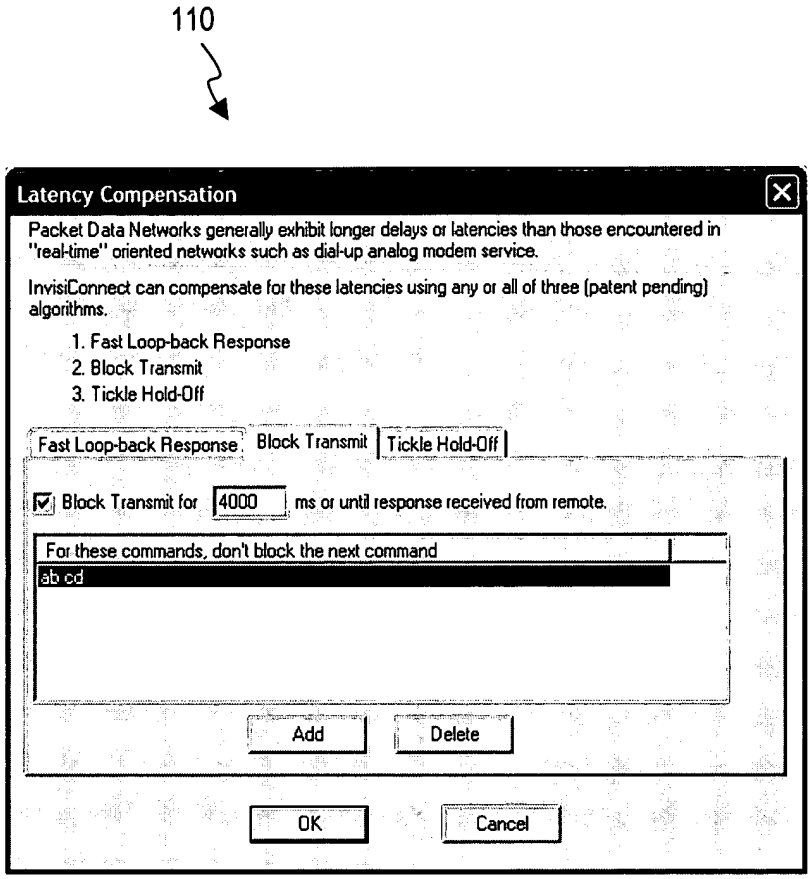

Fig. 12

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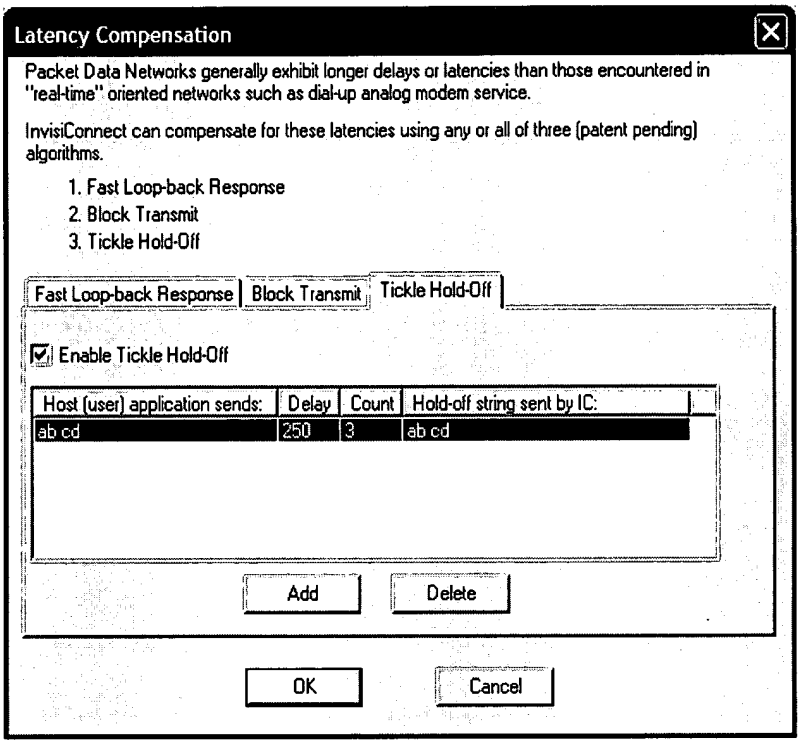

Fig. 13